

SECURITIES AND EXCHANGE COMMISSION

FORM 8-K

Current report filing

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FILER

MATHSTAR INC

CIK: **1118037** | IRS No.: **000000000** | State of Incorporation: **MN** | Fiscal Year End: **1231**
Type: **8-K** | Act: **34** | File No.: **000-51560** | Film No.: **09544707**
SIC: **3674** Semiconductors & related devices

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**UNITED STATES
SECURITIES AND EXCHANGE COMMISSION**

Washington, D.C. 20549

FORM 8-K

CURRENT REPORT

Pursuant to Section 13 Or 15(d) of The Securities Exchange Act of 1934

Date of Report (Date of earliest event reported) **January 26, 2009**

MathStar, Inc.

(Exact name of registrant as specified in its charter)

Delaware

(State or other jurisdiction
of incorporation)

000-51560

(Commission
File Number)

41-1881957

(IRS Employer
Identification No.)

19075 N.W. Tanasbourne Drive, Suite 200, Hillsboro, OR

(Address of principal executive offices)

97124

(Zip Code)

Registrant's telephone number, including area code **(503) 726-5500**

(Former name or former address, if changed since last report.)

Check the appropriate box below if the Form 8-K filing is intended to simultaneously satisfy the filing obligation of the registrant under any of the following provisions (see General Instructions A.2. below):

- Written communications pursuant to Rule 425 under the Securities Act (17 CFR 230.425)
- Soliciting material pursuant to Rule 14a-12 under the Exchange Act (17 CFR 240.14a-12)
- Pre-commencement communications pursuant to Rule 14d-2(b) under the Exchange Act (17 CFR 240.14d-2(b))
- Pre-commencement communications pursuant to Rule 13e-4(c) under the Exchange Act (17 CFR 240.13e-4(c))

Section 7 - Regulation FD

Item 7.01 Regulation FD Disclosure.

OMB APPROVAL

OMB Number: 3235-0060

Expires: April 30, 2009

Estimated average burden
hours per response. . . 5.0

On January 26, 2009, MathStar, Inc. (“MathStar”) and Core Capital Group issued a press release regarding the sale of MathStar’s technology and intellectual property. A copy of the press release is attached as Exhibit 99.1.

The information in this Current Report on Form 8-K, including the exhibit, shall not be deemed “filed” for the purposes of Section 18 of the Securities Exchange Act of 1934, as amended (“Exchange Act”), or otherwise subject to the liabilities under that Section. Furthermore, the information in this Current Report on Form 8-K, including the exhibit, shall not be deemed to be incorporated by reference into the filings of MathStar under the Exchange Act or the Securities Act of 1933, as amended.

Section 9 - Financial Statements and Exhibits

Item 9.01. Financial Statements and Exhibits.

(d) Exhibits.

The following exhibit is being furnished with this Current Report on Form 8-K:

99.1 A copy of the press release issued on January 26, 2009.

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SIGNATURES

Pursuant to the requirements of the Securities Exchange Act of 1934, the registrant has duly caused this report to be signed on its behalf by the undersigned hereunto duly authorized.

MathStar, Inc.

Date: January 26, 2009.

By /s/ Douglas M. Pihl

Chief Executive Officer and
Chief Financial Officer
(Principal Executive Officer and
Principal Financial Officer)

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Exhibit Index

99.1 A copy of the press release issued on January 26, 2009.

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NEWS RELEASE

FOR IMMEDIATE RELEASE

MathStar Has Prepared FPOA Technology Package for Purchase

Semiconductor company offers webinar to view Arrix MOA3600 tool set

COLORADO SPRINGS, Colorado - January 26, 2009 - Core Capital's Electronics and Semiconductor Group (ESG), an investment banking firm with an electronics and semiconductor focus, announces that its client, MathStar, Inc. (OTC: MATH), has prepared its technology for purchase. MathStar, a fabless semiconductor company specializing in high-performance programmable logic, is finalizing its Field Programmable Object Array (FPOA) technology and intellectual property package for sale. Arrangements have been prepared to assist prospective buyers to evaluate the opportunity, including a webinar, a private "data room" web portal, and access to key creators of the technology.

MathStar began its sales efforts in early October 2008, when it signed with the Core Capital Electronics and Semiconductor Group to complete a strategic sale of MathStar's technology and intellectual property.

Technology

After receiving customer feedback on its second generation products, the Arrix MOA2400, MathStar continued work to make its FPOA technology easier to use, develop and deploy. The latest version of the technology, the Arrix MOA3600, had been designed and was close to final tapeout when MathStar curtailed its development work on it. This updated IP, along with the current production products, are now ready for sale, including design documentation to take the purchaser to the next step.

The Arrix MOA3600 extends the market-leading performance of the Arrix MOA2400 and features a more user-friendly tool chain using industry standard Verilog. The Arrix MOA3600 technology package includes the alpha release version and the updated tool set that is significantly easier to use and more productive than the previous version.

Webinar and Data Room

MathStar is now offering a webinar-based demonstration that enables real-time displays of the updated Arrix MOA3600 design tools. The webinar is available to parties interested in evaluating the company, its IP and technology.

Additionally, MathStar has created an online data room containing a large number of detailed architecture, design and application documents to assist prospective buyers in understanding the depth and scope of the technology. Included in the data room is detailed information that has not previously been announced by MathStar including:

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- a) A family of two FPOA products designed for TSMC 90nm CMOS, each of which includes 600 object arrays (3600). One product family is optimized for very high bandwidth video processing and the other for very high speed data processing.
 - b) The introduction of an extremely high-performance Sum of Absolute Differences (SAD) object that enables industry-leading predictive motion computations for video processing and image processing applications.

- c) Significant advancements in the programming tools environment, based on an industry standard platform, making application development and reuse easier.

Prospective buyers can attend the webinar or access the online data room by contacting Loren Lancaster at 719-598-4680, or by email llancaster@corecapital.net.

Availability

Used by multiple customers in computationally-intensive and broadcast video applications, MathStar' s IP cores speed time to market by enabling quick and efficient deployment of performance-demanding FPOA designs.

MathStar' s technology and IP package are now available for sale. MathStar has created a package that will make it possible for a buyer to take advantage of the existing products and allow continued rapid development of the technology. The package may also include consulting services and a liaison with MathStar to facilitate a speedy and successful transfer. MathStar has one of the creators of the technology under contract to facilitate the evaluation and sale of the technology. The makeup of the complete transfer team will be determined by the prospective buyer and its needs.

For more information on MathStar, contact Loren Lancaster at 719-598-4680, or email llancaster@corecapital.net.

MathStar, Inc.

MathStar (OTC: MATH) is a fabless semiconductor company offering best-in-class, high performance programmable logic solutions. MathStar' s field programmable object array (FPOA) technology can process arithmetic and logic operations at clock rates of 1-gigahertz, which is up to four times faster than even the most advanced FPGA architectures in many applications. MathStar' s Arrix family of FPOAs are high-performance programmable solutions that enable customers in the machine vision, high-performance video, medical imaging, security and surveillance, and military markets to rapidly and cost effectively innovate and differentiate their products. FPOAs are available now and are supported by development tools, IP cores, application notes and technical documentation.

Core Capital ESG

Core Capital' s Electronics and Semiconductor Group focuses exclusively on providing a range of merger and acquisition services to middle market electronics and semiconductor companies. Core Capital is a member company of the International Network of M&A Partners (IMAP), a global partnership of 60 leading merger & acquisition advisory firms located in over 40 countries worldwide. For more information, go to www.ESGibank.com.

Statements in this press release, other than historical information, may be "forward-looking" in nature within the meaning of Section 21E the Private Securities Litigation Reform Act of 1995 and are subject to various risks, uncertainties and assumptions. These statements are based on management' s current expectations, estimates and projections about MathStar and its industry and include, but are not limited to, those set forth in the section of MathStar' s Annual Report on Form 10-K filed with the Securities and Exchange Commission on March 14, 2008 under the heading "Risk Factors." MathStar undertakes no obligation to update any forward-looking statements in order to reflect events or circumstances that may arise after the date of this release.